



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/976,708

Filed: October 12, 2001

Inventor(s):

Michael C. Dorsey

§ Examiner: Trimmings, J.

§ Group/Art Unit: 2133

§ Atty. Dkt. No: 5681-55500

Title: ASIC BIST
CONTROLLER
EMPLOYING MULTIPLE
CLOCK DOMAINS

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Erik A. Heter
Printed Name
Signature _____ Date June 8, 2004

SUBMISSION OF FORMAL DRAWINGS RECEIVED

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Technology Center 2100

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,

Erik A. Heter
Reg. No. 50,652
AGENT FOR APPLICANT(S)

Meyertons, Hood, Kivlin, Kowert, & Goetzel, P.C.
P.O. Box 398
Austin, TX 78767-0398
Phone: (512) 853-8800
Date: June 8, 2004



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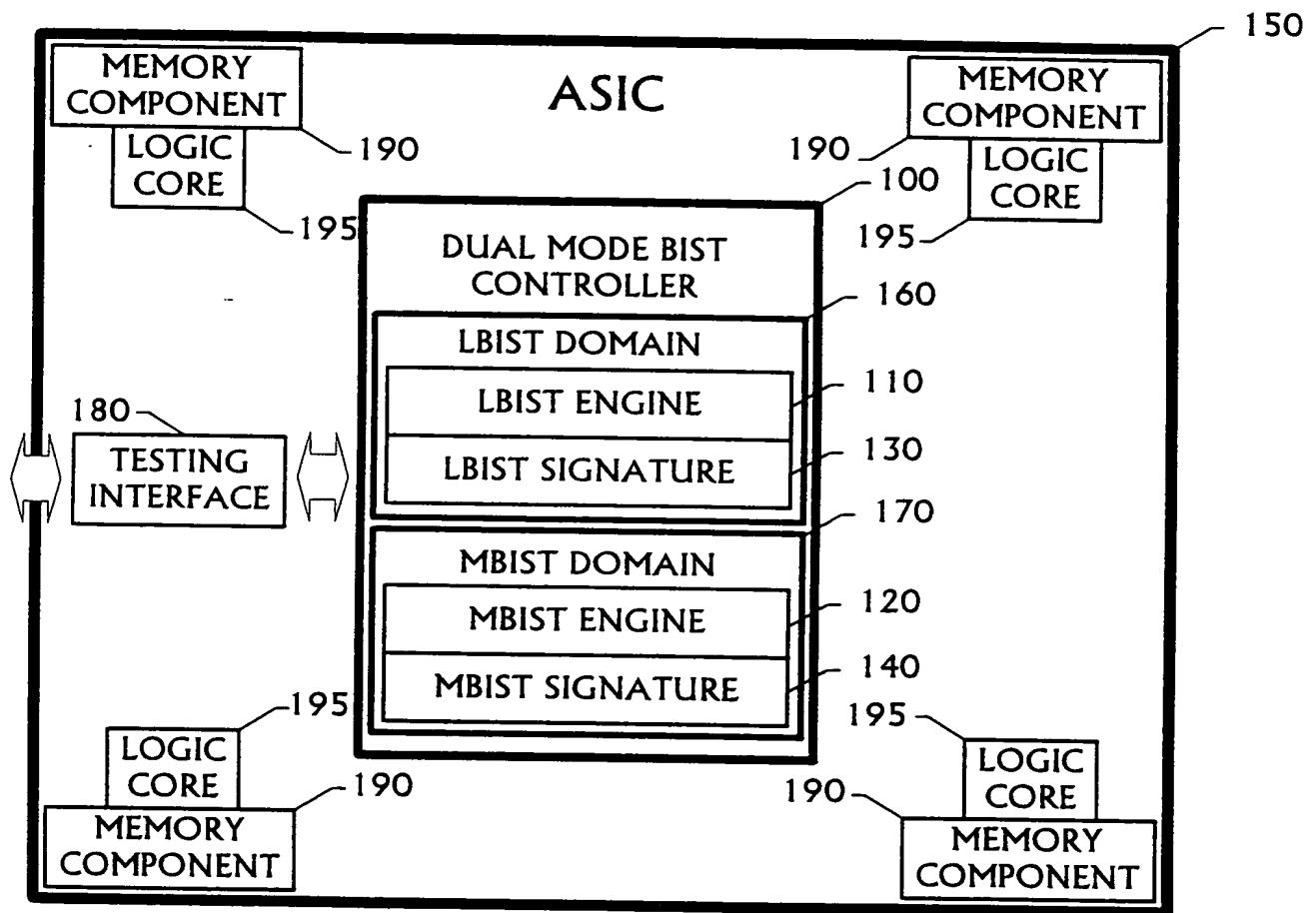


FIG. 1



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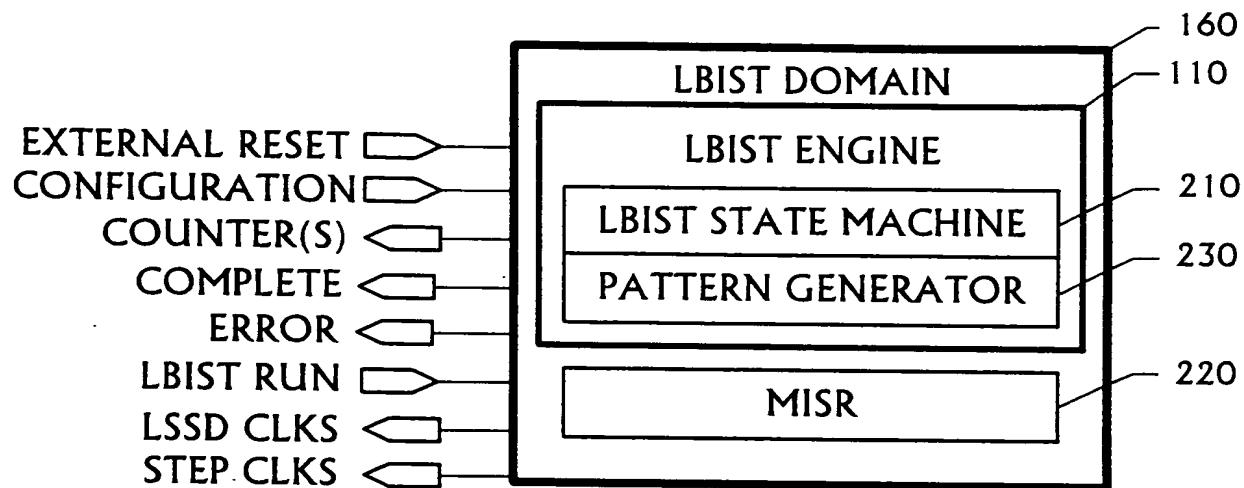


FIG. 2

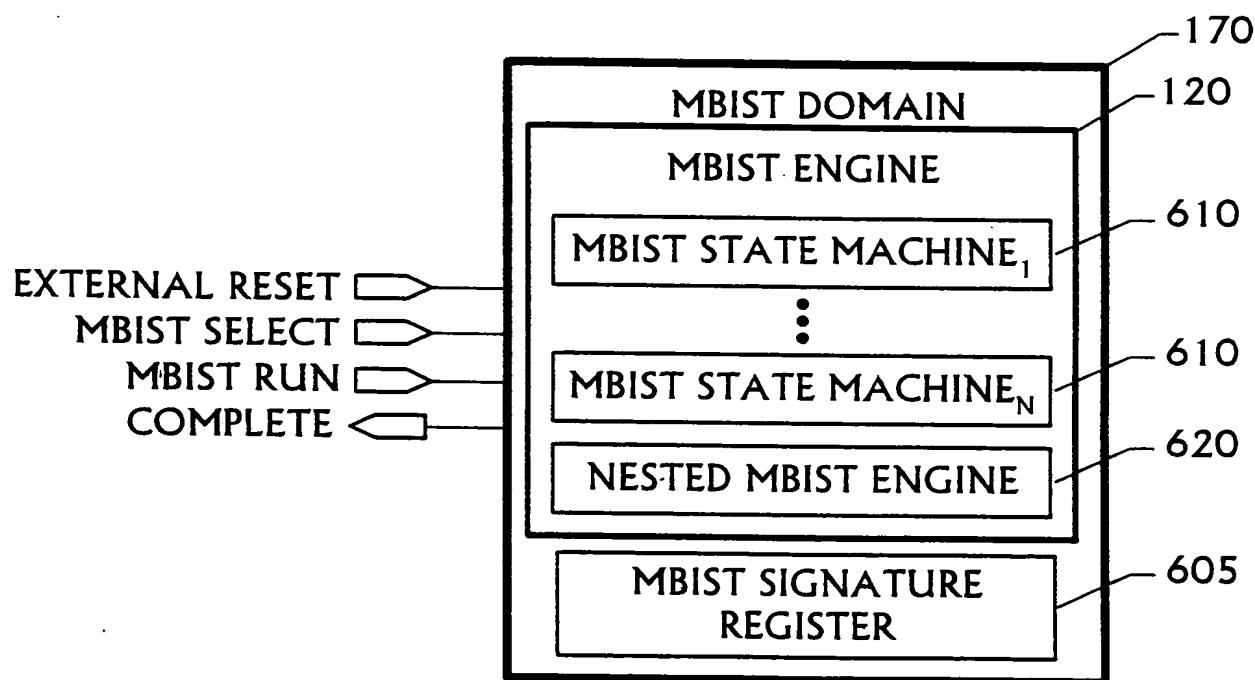


FIG. 6



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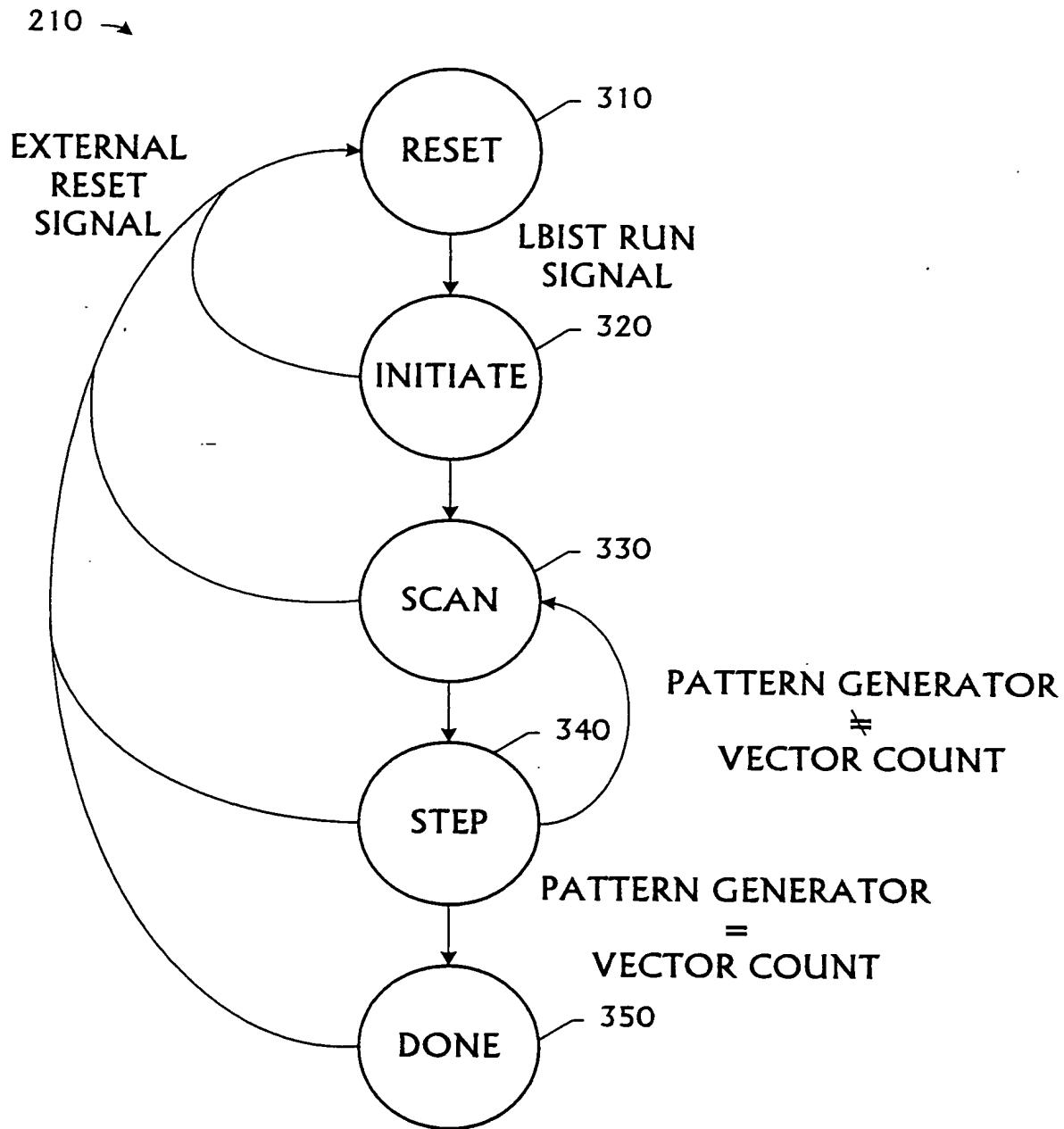


FIG. 3



FIG. 4

B ₃₁	B ₃₀	B ₂₉	B ₂₈	B ₂₇	B ₂₆	B ₂₅	B ₂₄	B ₂₃	B ₂₂	B ₂₁	B ₂₀	B ₁₉	B ₁₈	B ₁₇	B ₁₆	B ₁₅	B ₁₄	B ₁₃	B ₁₂	B ₁₁	B ₁₀	B ₉	B ₈	B ₇	B ₆	B ₅	B ₄	B ₃	B ₂	B ₁	B ₀
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FIG. 5

B ₃₀	B ₂₉	B ₂₈	B ₂₇	B ₂₆	B ₂₅	B ₂₄	B ₂₃	B ₂₂	B ₂₁	B ₂₀	B ₁₉	B ₁₈	B ₁₇	B ₁₆	B ₁₅	B ₁₄	B ₁₃	B ₁₂	B ₁₁	B ₁₀	B ₉	B ₈	B ₇	B ₆	B ₅	B ₄	B ₃	B ₂	B ₁	B ₀
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B ₃₁	B ₃₀	B ₂₉	B ₂₈	B ₂₇	B ₂₆	B ₂₅	B ₂₄	B ₂₃	B ₂₂	B ₂₁	B ₂₀	B ₁₉	B ₁₈	B ₁₇	B ₁₆	B ₁₅	B ₁₄	B ₁₃	B ₁₂	B ₁₁	B ₁₀	B ₉	B ₈	B ₇	B ₆	B ₅	B ₄	B ₃	B ₂	B ₁	B ₀
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FIG. 7



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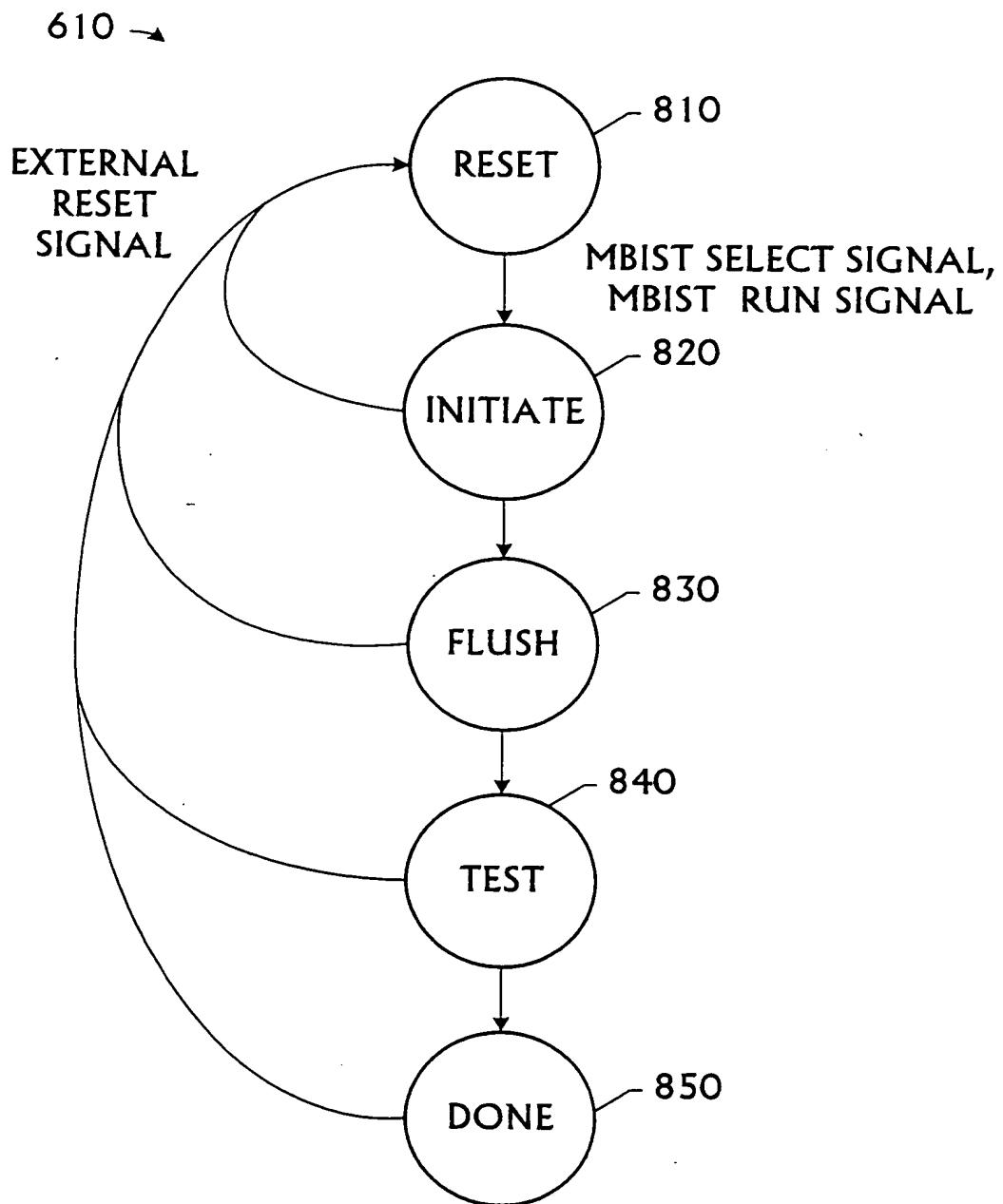


FIG. 8



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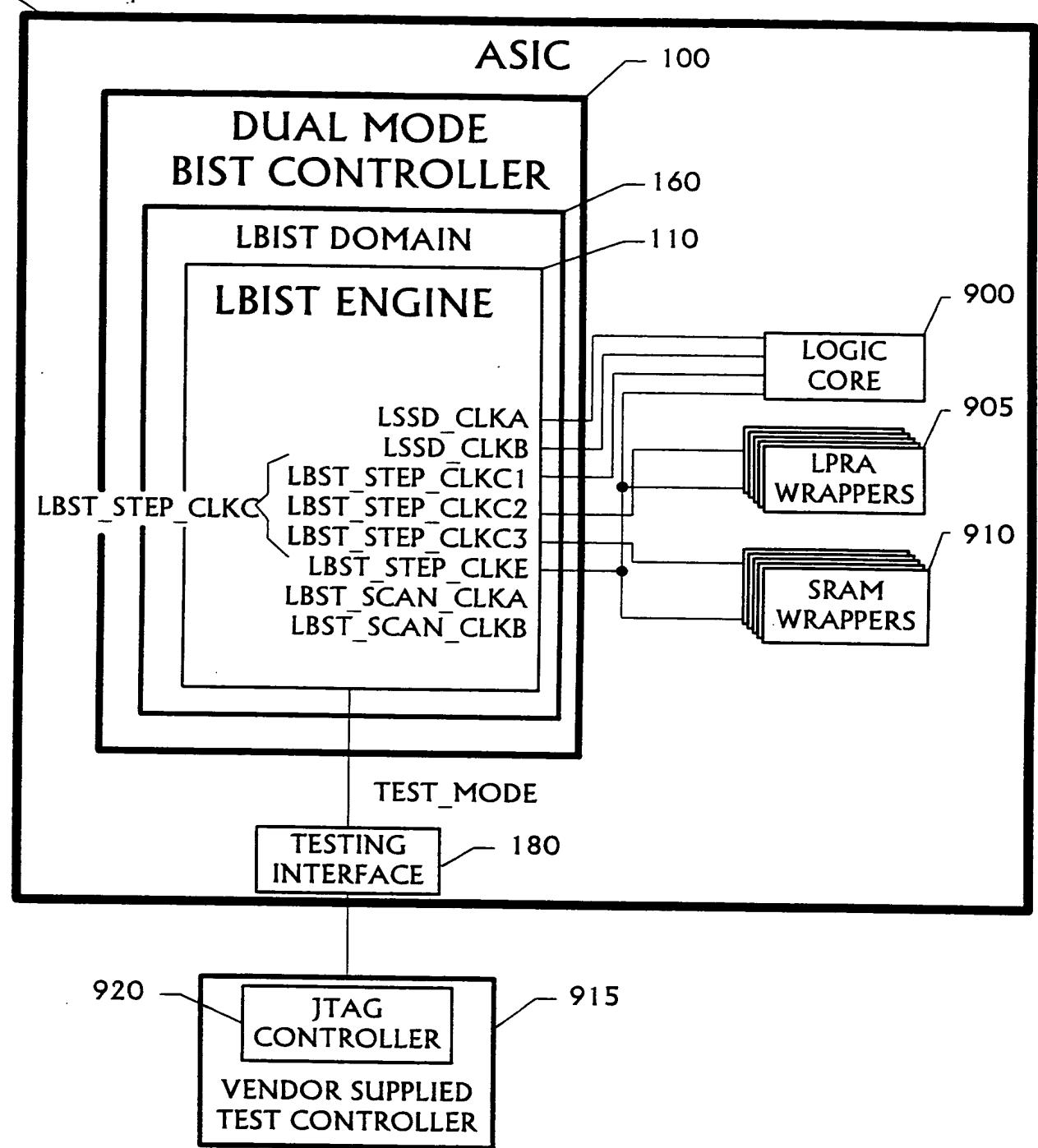


FIG. 9



Replacement Sheet

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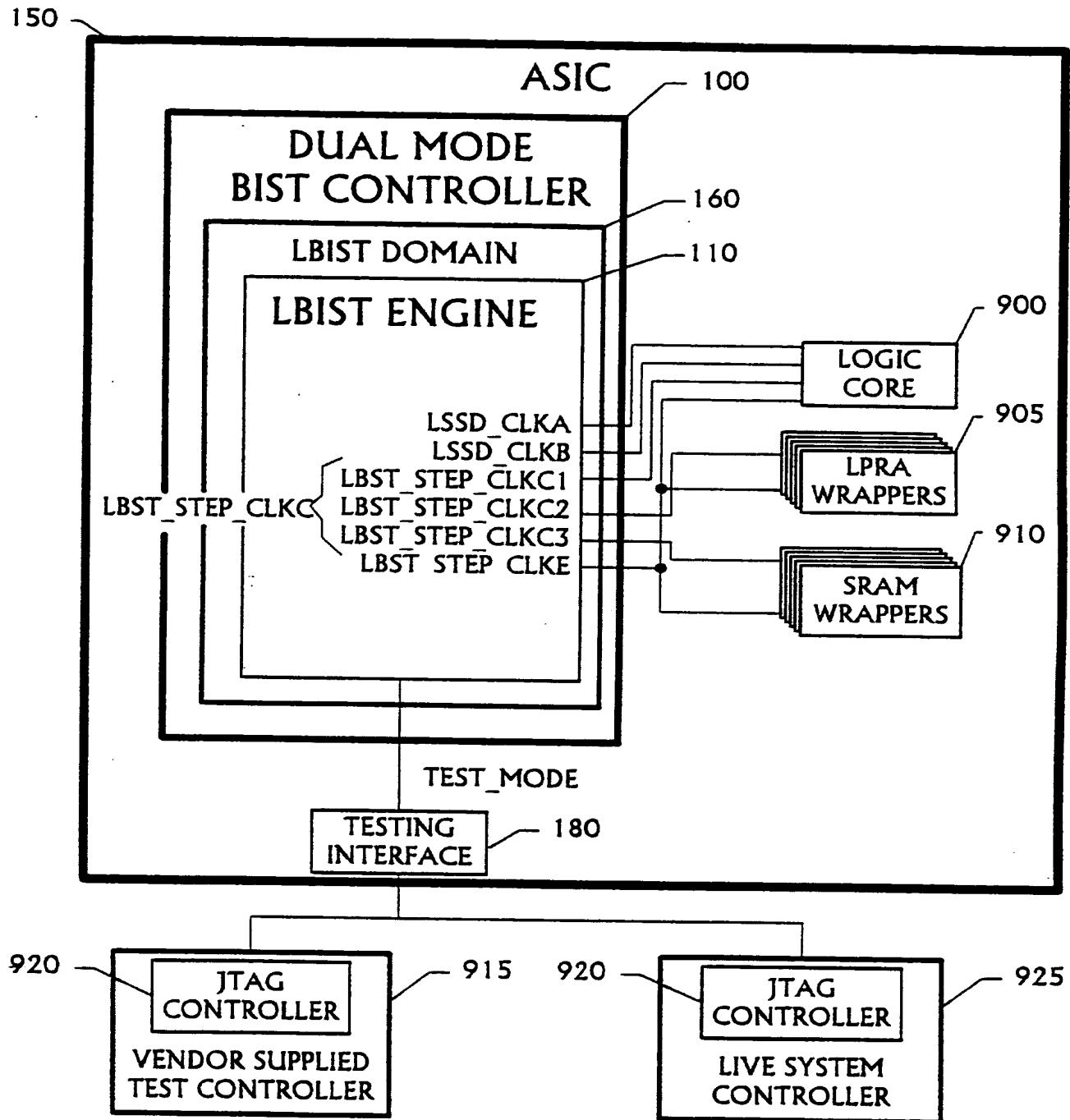


FIG. 9